

# Trace management and analysis with FrameSoC

**Generoso Pagano** 

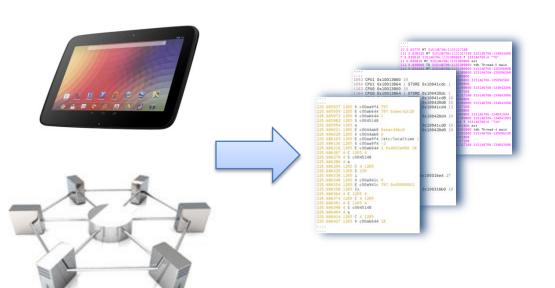
generoso.pagano@inria.fr

Journées Mescal

Autrans, 18/03/2014

# Application tracing

- Powerful instrument to analyze application behavior
  - Embedded Systems, Parallel computing, HPC, ...
- Limited intrusivity
- Help debugging and profiling

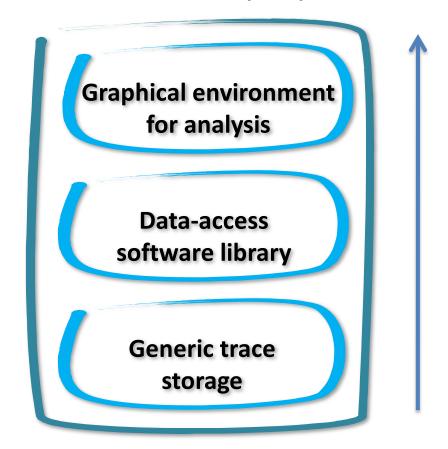


#### **ISSUES**

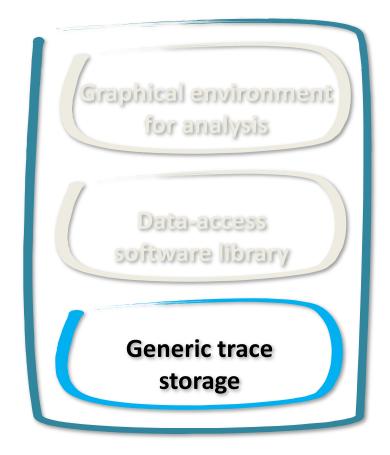
- Different formats
- Different analysis
- Data volume
- Visualization scalability

## FrameSoC: trace analysis infrastructure

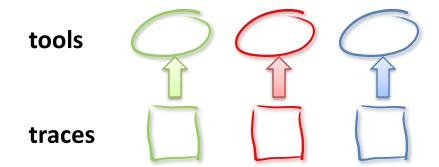
To tackle the above issues we propose FrameSoC



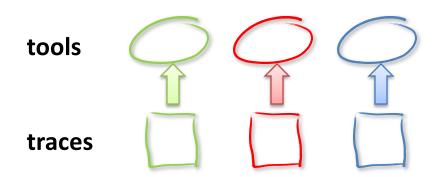
- FrameSoC is developed within the SoC-Trace project
  - INRIA, UJF, STMicroelectronics, ProbaYes, Magillem



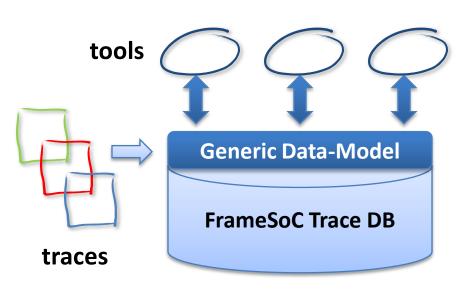
# Many formats, many tools



# Many formats, many tools



#### **FrameSoC**





- Represent different trace formats
  - Self defining approach
  - Event categories
- Store various trace metadata
- Store trace analysis results

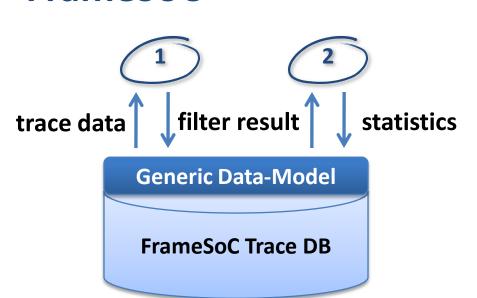
# Analysis workflow



# Analysis workflow

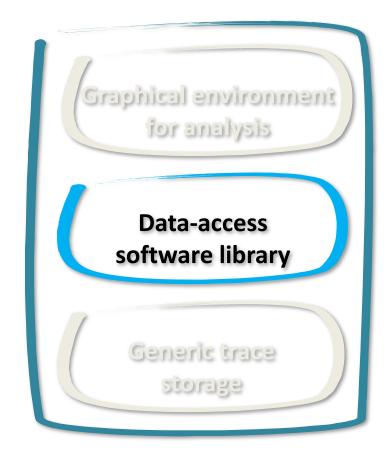


#### **FrameSoC**





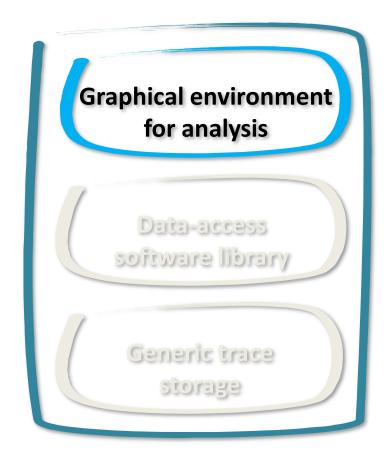
- Tool cooperation
- Avoid long recomputations



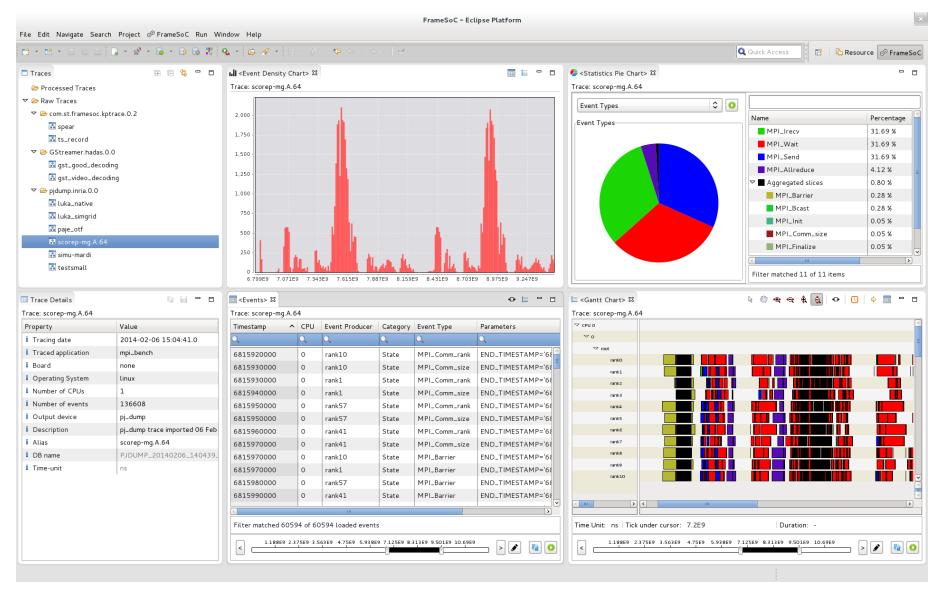
## **SoC-Trace Library**

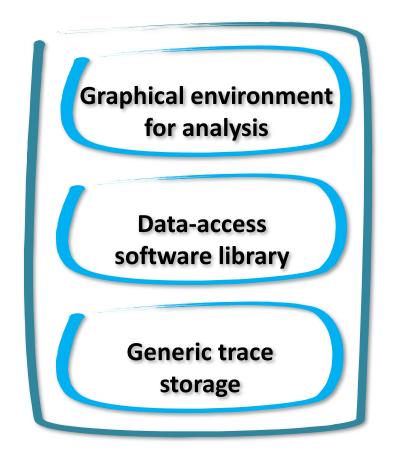


- It is a set of Eclipse plugins
- Read/Write access to data abstracts storage details
  - We read and write Java objects
- Factorization of trace-access functionalities
  - Basic bricks to build analysis tools (e.g. *Ocelotl*)



#### FrameSoC Workbench

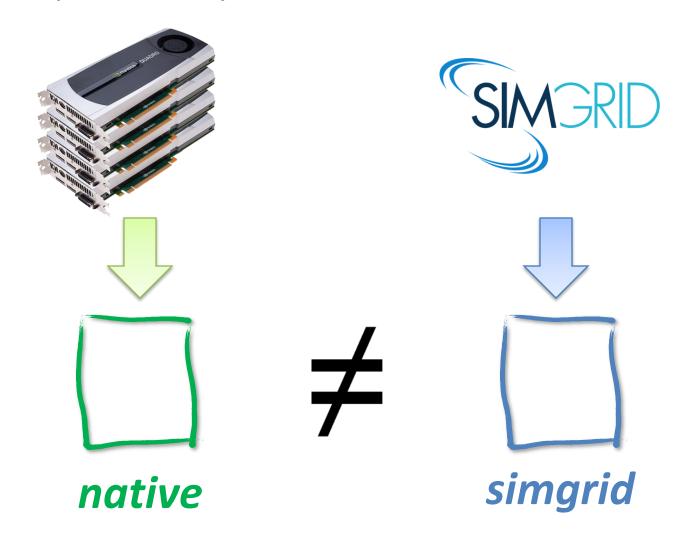




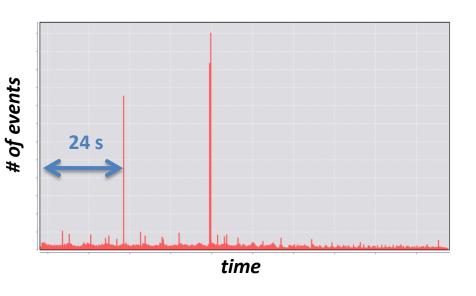
#### Let's see FrameSoC in action!

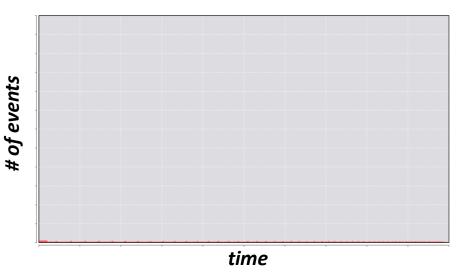
## An example: comparison of two traces

Use case provided by Luka

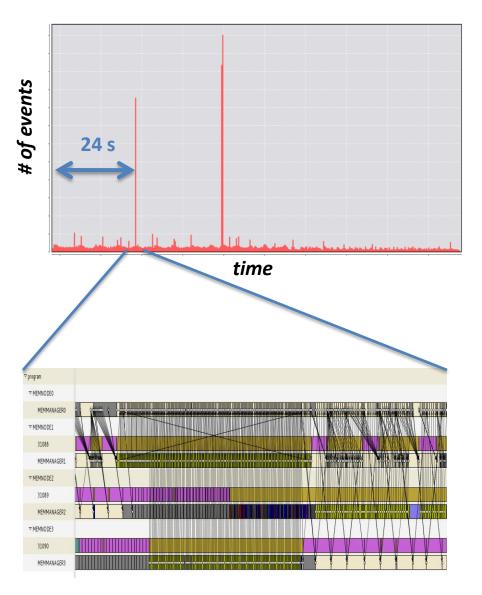


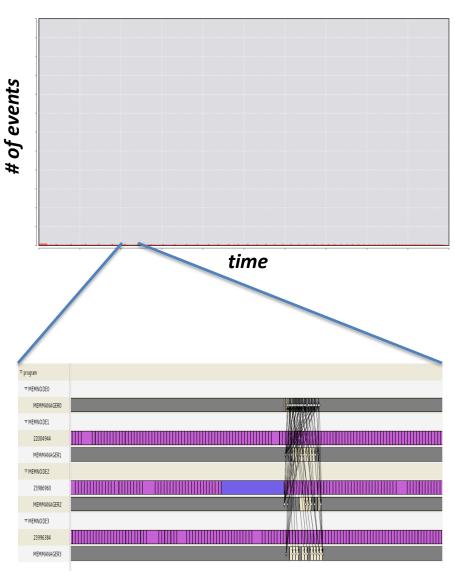
## simgrid





## simgrid



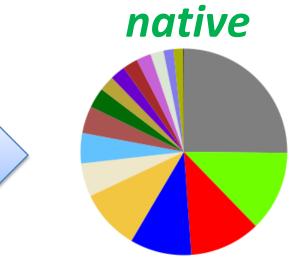


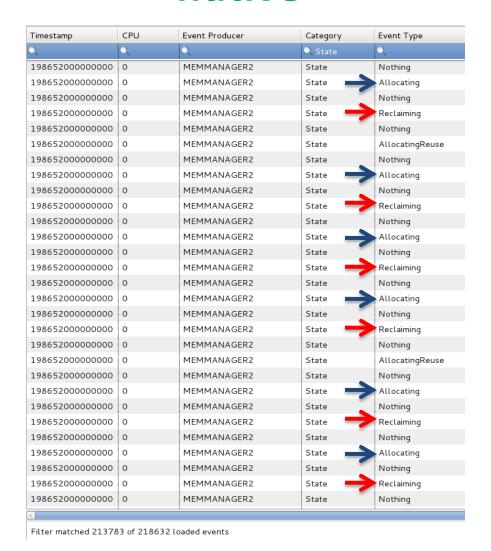
Timestamp	CPU	Event Producer	Category	Event Type
Q.	Q	Q	State	Q
198652000000000	0	MEMMANAGER2	State	Nothing
198652000000000	0	MEMMANAGER2	State	Allocating
198652000000000	0	MEMMANAGER2	State	Nothing
198652000000000	0	MEMMANAGER2	State 🛑	Reclaiming
198652000000000	0	MEMMANAGER2	State	Nothing
198652000000000	0	MEMMANAGER2	State	AllocatingReuse
198652000000000	0	MEMMANAGER2	State	Nothing
198652000000000	0	MEMMANAGER2	State	Allocating
198652000000000	0	MEMMANAGER2	State	Nothing
198652000000000	0	MEMMANAGER2	State 🔫	Reclaiming
198652000000000	0	MEMMANAGER2	State	Nothing
198652000000000	0	MEMMANAGER2	State 📄	Allocating
198652000000000	0	MEMMANAGER2	State	Nothing
198652000000000	0	MEMMANAGER2	State 🛑	Reclaiming
198652000000000	0	MEMMANAGER2	State	Nothing
198652000000000	0	MEMMANAGER2	State	Allocating
198652000000000	0	MEMMANAGER2	State	Nothing
198652000000000	0	MEMMANAGER2	State 🛑	Reclaiming
198652000000000	0	MEMMANAGER2	State	Nothing
198652000000000	0	MEMMANAGER2	State	AllocatingReuse
198652000000000	0	MEMMANAGER2	State	Nothing
198652000000000	0	MEMMANAGER2	State	Allocating
198652000000000	0	MEMMANAGER2	State	Nothing
198652000000000	0	MEMMANAGER2	State 💮	Reclaiming
198652000000000	0	MEMMANAGER2	State	Nothing
198652000000000	0	MEMMANAGER2	State 📥	Allocating
198652000000000	0	MEMMANAGER2	State	Nothing
198652000000000	0	MEMMANAGER2	State 🛑	Reclaiming
198652000000000	0	MEMMANAGER2	State	Nothing

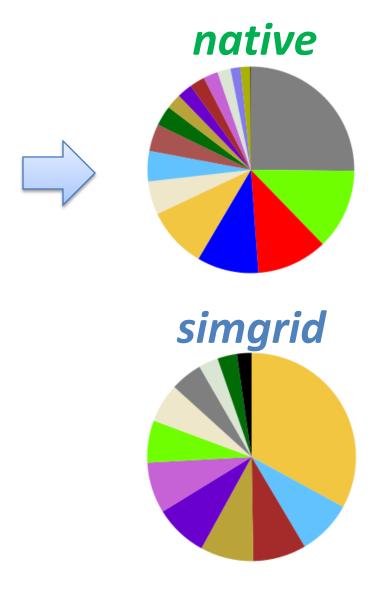
Filter matched 213783 of 218632 loaded events

Timestamp	CPU	Event Producer	Category	Event Type
`	Q	Q	🔍 State	Q
198652000000000	0	MEMMANAGER2	State	Nothing
198652000000000	0	MEMMANAGER2	State	Allocating
198652000000000	0	MEMMANAGER2	State	Nothing
198652000000000	0	MEMMANAGER2	State 🛑	Reclaiming
198652000000000	0	MEMMANAGER2	State	Nothing
198652000000000	0	MEMMANAGER2	State	AllocatingReuse
198652000000000	0	MEMMANAGER2	State	Nothing
198652000000000	0	MEMMANAGER2	State	Allocating
198652000000000	0	MEMMANAGER2	State	Nothing
198652000000000	0	MEMMANAGER2	State 👈	Reclaiming
198652000000000	0	MEMMANAGER2	State	Nothing
198652000000000	0	MEMMANAGER2	State	Allocating
198652000000000	0	MEMMANAGER2	State	Nothing
198652000000000	0	MEMMANAGER2	State	Reclaiming
198652000000000	0	MEMMANAGER2	State	Nothing
198652000000000	0	MEMMANAGER2	State	Allocating
198652000000000	0	MEMMANAGER2	State	Nothing
198652000000000	0	MEMMANAGER2	State 🛑	Reclaiming
198652000000000	0	MEMMANAGER2	State	Nothing
198652000000000	0	MEMMANAGER2	State	AllocatingReuse
198652000000000	0	MEMMANAGER2	State	Nothing
198652000000000	0	MEMMANAGER2	State	Allocating
198652000000000	0	MEMMANAGER2	State	Nothing
198652000000000	0	MEMMANAGER2	State	Reclaiming
198652000000000	0	MEMMANAGER2	State	Nothing
198652000000000	0	MEMMANAGER2	State	Allocating
198652000000000	0	MEMMANAGER2	State	Nothing
198652000000000	0	MEMMANAGER2	State 👈	Reclaiming
198652000000000	0	MEMMANAGER2	State	Nothing

Filter matched 213783 of 218632 loaded events







- Actual problem: simgrid assumed infinite GPU memory
  - no swapping to the RAM occurred during simulation

#### Conclusion...

- FrameSoC can facilitate trace analysis
  - Base analysis tools
  - Fast analysis
  - Hints to find the problem

### ...and Perspectives

- Type/Producer filtering in Pie and Histogram
- Time filtering in Pie
- Provide more statistical views
- Improve ergonomics
- **4** ...



First open-source release of FrameSoC in June 2014

# Questions?

